Electronic Patent A	۱pp	lication Fe	e Transı	mittal		
Application Number:	10	10552948				
Filing Date:						
Title of Invention:	Read and Erase Verify Methods and Circuits Suitable for Low Voltage Non-Volatile Memoriles					
First Named Inventor:	Jian Chen					
Filer:	Katherine Alison DeRuntz					
Attorney Docket Number:	SNDK.284US1					
Filed as Large Entity						
U.S. National Stage under 35 USC 371 Fill	ing	Fees				
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)	
Basic Filing:						
Pages:						
Claims:						
Miscellaneous-Filing:						
Petition:						
Patent-Appeals-and-Interference:						
Post-Allowance-and-Post-Issuance:						
Extension-of-Time:						
Extension - 2 months with \$0 naid		1252	1	450	450	

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Total in USD (\$)			450